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Application No.: 10/828,408  
Filing Date: April 19, 2004  
Inventor: LAKSHMANAN, Viswanathan  
Art Unit: 2825  
Confirmation No.: 4517  
Examiner: Parihar, Suchin

From: Eric J. Whitesell #38657

Today's Date: April 10, 2006  
Attorney Docket No.: 03-2644 81693  
Pages: 13 pages (including this 1-page coversheet)  
Dispatched by: Eric J. Whitesell

**Transmitted herewith for filing via facsimile:**

Transmittal (1 pg.)  
Amendment "A" (11 pgs.)

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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10/828,408	
	Filing Date	April 19, 2004	
	First Named Inventor	LAKSHMANAN, Viswanathan	
	Art Unit	2825	
	Examiner Name	Parihar, Suchin	
Total Number of Pages in This Submission	13	Attorney Docket Number	03-2644 81693

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Amendment "A" page 1 of 11  
10/828,408

DOCKET NO. 03-2644 81693

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): LAKSHMANAN,  
Viswanathan et al.  
Serial No.: 10/828,408  
Filed: April 19, 2004  
For: METHOD AND COMPUTER  
PROGRAM FOR VERIFYING AN  
INCREMENTAL CHANGE TO AN  
INTEGRATED CIRCUIT DESIGN  
Art Unit: 2825  
Examiner: Parihar, Suchin

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April 10, 2006 / Eric James Whitesell /  
Eric J. Whitesell #38657

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Office Action mailed on February 23, 2006.